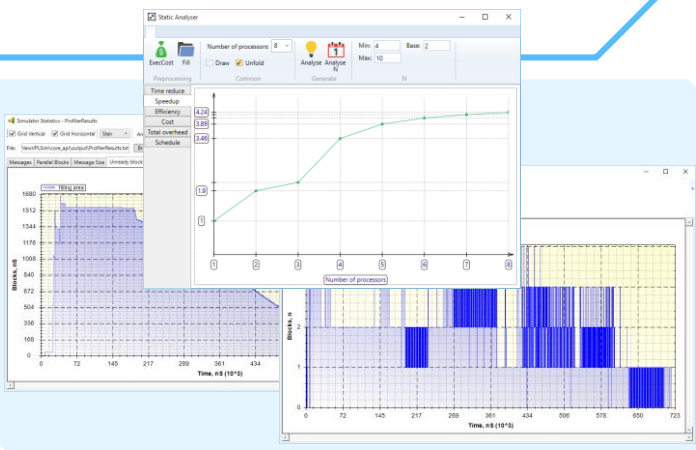


Additional tools

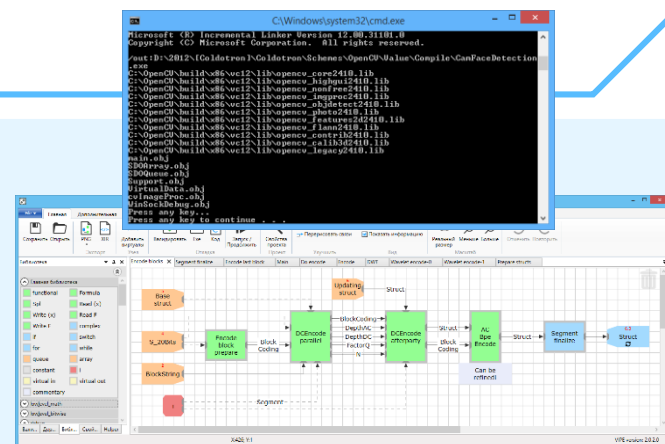
Performance estimation

Analysis of computing characteristics for developed visual programs. Tools provide rapid and effective methods for program characteristics estimation and evaluation through all stages of the program development process. Analysis methods allow to correct the development process according to given constraints and requirements. It also allows optimizing algorithm structures.



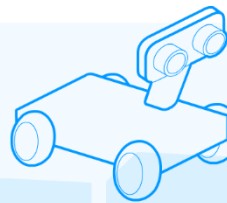
Code generation

Preparation and Integration of developed programs with target embedded heterogeneous manycore platforms. Code generation tools perform an allocation on different types of cores, provide integration methods, communication and synchronization and generate final code for correct execution in multitasking heterogeneous environment.



Domain experts edition

Microcontrollers



Video analytics

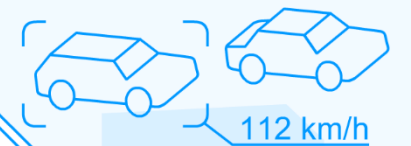
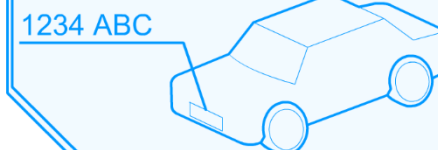


Image recognition



Add specific domain



VIPE

Developer

Saint Petersburg State University of
Aerospace Instrumentation
High Performance Computing and Network
Technologies department



Yuriy Sheynin
sheynin@annet.ru

67, Bolshaya Morskaya street,
Saint Petersburg, Russia
Phone: +7 (812) 710-62-34
Fax: +7 (812) 710-62-34
www.suai.ru

- Accomplished within the Federal Special Programme of Russian Federation, project "Technology and development tools of the portable embedded software for heterogeneous manycore embedded systems"
- Accomplished within the European technology platform ARTEMIS, project "PaPP"

Visual integrated environment for application programming

The VIPE environment provides full development cycle for domain specific programs from an algorithm to a final code, that is ready to upload into a target hardware platform. The high-level visual representation and domain specific libraries significantly accelerate the program development process.

See how to
develop in
VIPE on
Youtube



vkist@guap.ru



www.suai.ru

Fast visual high-level development of end-user solutions in VIPE

Create

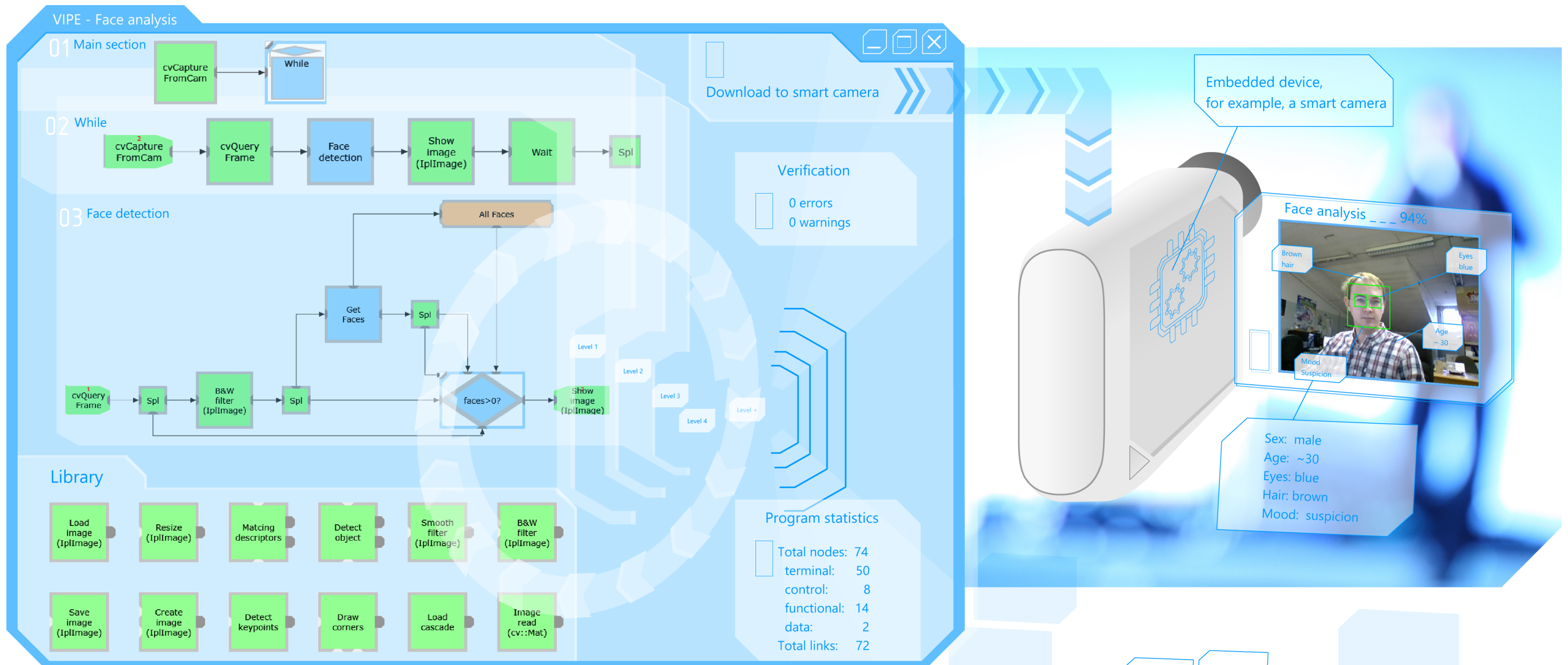
User works with a high-level visual parallel programming language. For a convenience, program schemes could be multi-layer, the program block could represent a sub-scheme.

Domain-specific libraries usage significantly accelerates a development process: it's enough to connect specific blocks together.

Debug and run

Environment automatically verifies the program during development process. If errors are detected, the program execution possibility is blocked and the error place is highlighted on the scheme.

Integrated tools provide easy debugging on the local PC, compiling and uploading of the program to various target hardware platforms.



Technology basis

VIPE uses the VPL language based on the formal parallel computation model AGP (Asynchronous Growing Process). AGP model is a result of fundamental researches in parallel computing, evolved during 20 years. It can carry the solution of complex problems that are concerned with parallel computations and heterogeneity of the target platform, allows user to focus exactly on solving application tasks.

See the development process in VIPE on Youtube

